

# SSC8337GN4

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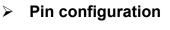
## **Dual P-Channel Enhancement Mode MOSFET**

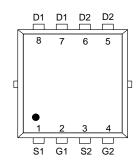
#### Features

V <sub>DS</sub>	V <sub>GS</sub>	R <sub>DS(ON)</sub> Typ.	ID	
-30V	+20V	16mΩ@-10V	-28A	
-30 v	<u> </u>	24mΩ@-4V5	-204	

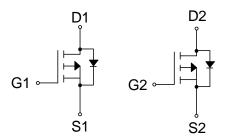
#### > Description

This SSC8337GN4 uses advanced trench technology to provide excellent RDSON and low gate charge. The complementary MOSFETS may be used to form a level shifted high side switch, and for a host of other applications.





#### PDFN3.3X3.3-8L (Top View)



#### Applications

- Load Switch
- DC/DC conversion
- Power Management in portable/desktop PCs

#### > Ordering Information

Device	Package	Shipping	
SSC8337GN4	PDFN3.3X3.3-8L	5000/Reel	





(YW: Internal Traceability Code)





Symbol	Parameter	Ratings	Unit		
V <sub>DSS</sub>	Drain-to-Source Voltage		-30	V	
V <sub>GSS</sub>	Gate-to-Source Voltag	Gate-to-Source Voltage		V	
	Continuous Drain Current d	Tc=25℃	-28		
ID Continuous Drain Current d	Tc=100℃	-15	A		
		T <sub>A</sub> =25℃	-10		
IDSM Continuous Drain Current <sup>a</sup>	T <sub>A</sub> =70℃	-7	A		
Ідм	Pulsed Drain Current <sup>b</sup>		-112	A	
5		Tc <b>=25</b> ℃	20	w	
PD	Power Dissipation <sup>c</sup>	Tc=100℃	8		
Pdsm	Power Dissipation <sup>a</sup>	T <sub>A</sub> =25℃	2.6	w	
		T <sub>A</sub> =70℃	1.67		
las	Avalanche Current <sup>b</sup> L=0.5mH Single Pulse		-18	A	
E <sub>AS</sub>	Avalanche Energy <sup>b</sup> L=0.5mH Single Pulse		81	mJ	
TJ	Operation junction temperature		-55~150	°0	
Tstg	Storage temperature range		-55~150	°C	

#### > Absolute Maximum Ratings ( $T_A=25^{\circ}C$ unless otherwise noted)

#### > Thermal Resistance Ratings ( $T_A=25^{\circ}C$ unless otherwise noted)

Symbol	Parameter	Ratings	Unit
R <sub>0JA</sub>	Junction-to-Ambient Thermal Resistance <sup>a</sup>	48	°C/W
Rejc	Junction-to-Case Thermal Resistance	6.25	C/W

Note:

- a. The value of R<sub>θJA</sub> is measured with the device mounted on 1 in<sup>2</sup> FR-4 board with 2oz.copper, in a still air environment with T<sub>A</sub>=25 °C. The value in any given application depends on the user is specific board design. The power dissipation is based on the t≤10s thermal resistance rating.
- b. Repetitive rating, pulse width limited by junction temperature.
- c. The power dissipation P<sub>D</sub> is based on T<sub>J(MAX)</sub>=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.
- d. The maximum current rating is package limited.

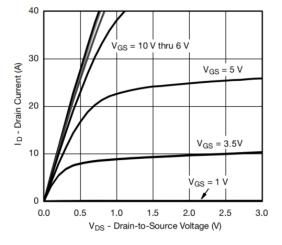


# > Electrical Characteristics (T<sub>A</sub>=25 $^{\circ}$ C unless otherwise noted)

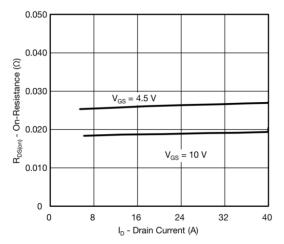
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V, I_D = -250 \mu A$	-30			V	
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250 uA$	-1	-1.6	-2.5	V	
Drain-Source On-Resistance	RDS(on) -	V <sub>GS</sub> = -10V, I <sub>D</sub> = -7A		16	23		
Drain-Source On-Resistance		V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -5A		24	34	mΩ	
Zero Gate Voltage Drain Current	loss	V <sub>DS</sub> = -30V, V <sub>GS</sub> = 0V			-1	μA	
Gate-Source Leak Current	lgss	$V_{GS}$ = ±20V, $V_{DS}$ = 0V			±100	nA	
Transconductance	$G_{FS}$	V <sub>DS</sub> = -5V, I <sub>D</sub> = -4A		15		s	
Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> = 0V, I <sub>S</sub> = -1A		-0.76	-1.3	V	
Input Capacitance	Ciss			1320			
Output Capacitance	Coss	$V_{DS} = -15V, V_{GS} = 0V,$ f = 1MHz		175		pF	
Reverse Transfer Capacitance	C <sub>RSS</sub>			158			
Total Gate Charge	Q <sub>G</sub>			25.5			
Gate to Source Charge	Q <sub>GS</sub>	V <sub>GS</sub> = -10V, V <sub>DS</sub> = -15V, I <sub>D</sub> = -6A		4.6		nC	
Gate to Drain Charge	$Q_{GD}$	ID = -0A		6.2		1	
Turn-on Delay Time	T <sub>D(ON)</sub>			7.8			
Rise Time	Tr	V <sub>GS</sub> =- 10V, V <sub>DS</sub> = -15V,		34.3			
Turn-off Delay Time	T <sub>D(OFF)</sub>	$R_L$ = 10 $\Omega$ , $R_{Gen}$ = 6 $\Omega$ ,		49.5		ns	
Fall Time	T <sub>f</sub>			10.9			



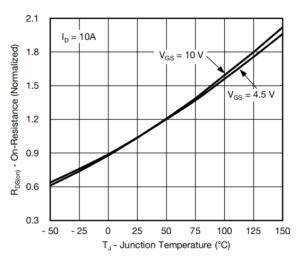
## > Typical Performance Characteristics ( $T_A=25^{\circ}C$ unless otherwise noted)



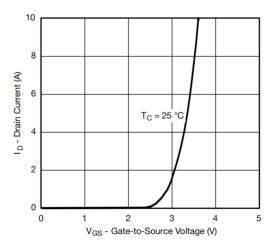




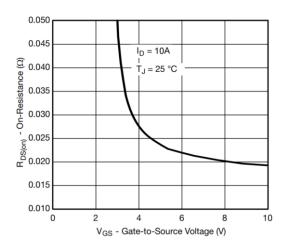
**On-Resistance vs. Drain Current and Gate Voltage** 



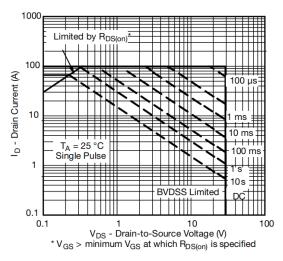
**On-Resistance vs. Junction Temperature** 



**Transfer Characteristics** 



On-Resistance vs. Gate-to-Source Voltage

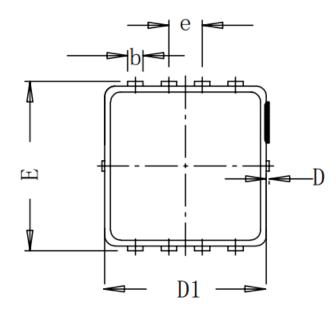


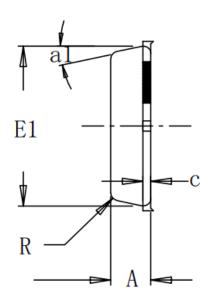
Safe Operating Area, Junction-to-Ambient

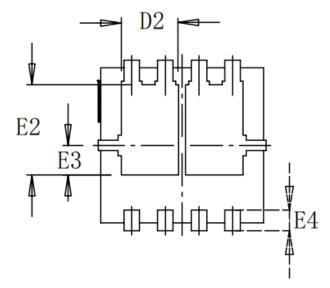
4 / 6



## > Package Information







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SYMBOL	MILLIMETER			
SIMDOL	MIN	NOM	MAX	
٨	0.75	0.78	0.81	
b	0.297	0.3	0.35	
C	-	0.152	-	
D	0.00	0.05	0.1	
D1	3.12	3.15	3.18	
D2	_	1.05	-	
E	32	33	3.4	
E1	3.09	3.12	3.15	
E2	-	1.75	-	
E3	-	0.575	-	
E4	-	0.4	-	
R	-	0.15	-	
e	0.65BSC			
al"	_	12"		



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